PTO/SB/08A (10-96)

Approved for use through 10/31/99. OMB 0851-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

	Sul	bstitute	for form	1449A/PTO	¥ 46	Com	plete if Known	
A						Application Number	09/511,737	
	INFOF	RMA'	TION	DISCLOSURE		Filing Date	February 24, 2000	
	STAT	EME	NT B	Y APPLICANT		First Named Inventor	YELLIN, Daniel	
						Group Art Unit	2124	
	(use	es ma	ny she	ets as necessary)		Examiner Name	MAI, Tan V.	77777
Sheet		1	of	2		Attorney Docket Number	P-5332-US	

		Ü.	S. PATENT DOCUMENTS		
Oit-	U.S. Patent Document			Date of Publication of	Pages, Columns, Lines. Where Relevant Passages or Relevant Figures Appear
No.'	Number	Number Kind Code ² of Cited Document (if known)	of Cited Document	MM-DD-YYYY.	Passages or Relevant Figures Appear
A	4,982,355		Nishimura et al.	01-01-1991	
				<u> </u>	
	 				
				 	<u> </u>
	 				
	 				
	Cite No.¹	Cite No. 1 Number	Cite Number Kind Code ² (if known)	Cite No. 1 Number Kind Code ² (if known) Name of Patentee or Applicant of Cited Document A 4,982,355 Nishimura et al.	Cite Number Kind Code (if known) A 4,982,355 Nishimura et al. Date of Publication of Cited Document MM-DD-YYYY. Nishimura et al. Date of Publication of Cited Document MM-DD-YYYY.

				FORE	IGN PATENT DOCUM	ENTS		
Examiner Initials*	Cite No.1	Foreig	nPalent	Document	Name of Patentee or	Date of Publication of	tion of Pages, Columns. Lines.	
		Office ³ Number ⁴ (if known)		Kind Code ⁵ (if known)	Applicant of Cited Document	Cited Docum MM-DD-YY	Where Relevant Pessages or Relevant Figures Appear	7
		١						
	<u> </u>							
			<u> </u>					_
	<u> </u>							
	<u> </u>	<u> </u>	<u> </u>					
	<u></u>	يـلــ						
Examine		******		1 1100	Dat	e nsidered	12/2004	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS SEND TO: Assistant Commissioner for Patents. Washington. DC 20231

Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached

PTO/SB/08A (10-96)

Approved for use through 10/31/99 OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

			7. 3	NI STATE OF THE ST		
	Substitu	te for fo	om 1449B/PTO	Complete if Known		
				Application Number	09/511,737	
INFO	RMA	TION	I DISCLOSURE	Filing Date	February 24, 2000	
STAT	STATEMENT BY APPLICANT			First Named Inventor	YELLIN, Daniel	
				Group Art Unit	2124	
(us	e as ma	any sh	eets as necessary)	Examiner Name	MAJ, Tan V.	
Sheet	2	of	2	Attorney Docket Number	P-5332-US	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Cite item (book, initials* No.1		Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	В	UMING KO et al., "A Self-Timed Method to Minimize Spurious Transitions in Low Power CMOS Circuits", Low	
		Power Electronics, 1994, Digest of Technical Papers, IEEE Symposium, San Diego CA, USA, 10 OCT 1984, pp.	
Van		62-63	
	С	Vivek Tiwari, "Guarded evaluation: pushing power management to logic synthesis/design", IEEE Transactions	
		on Computer-Aided Design of Integrated Circuits and Systems, Vol. 17, No. 10, 10 OCT 1998, pp. 1051-1060	
<i>V</i>	D	Supplementary Search Report for EP 01 90 6088	

Examiner Signature	hai	Date Considered	12/2004
-----------------------	-----	--------------------	---------

[•] EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered include copy of this form with next communication to applicant

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer. Patent and Trademark Office, Washington, DC 2023 1 DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents. Washington, DC 2023 1.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English tanguage Translation is attached